In September 2020, EDS Region 9 launched the EDS ASIC Design Contest. Thirteen proposals out 15 proposals from 6 different Region 9 countries were selected. EDS approved US$25K to cover the fabrication with the 180 nm Europractice TSMC MPW.

1. Conditioning system and digital biomedical signal, Mexico.
2. Receiver for Internet of Medical Things (IoMT) and ADC Converters, Mexico.
3. Low Power circuits for Biomedical Applications, Brazil.
4. A RISC-V Based MCU With a 15000 Tuning Range Feed-Forward Ring Oscillator for Multi-Protocol Support Applications, Colombia.
6. FEI2020 and ProjectFEI2020, Brazil.
7. Low Power Analog and Mixed-Signal blocks for IoT interfaces, Brazil.
8. Passive RFID reading range enhancement using energy harvesting techniques, Uruguay.
9. ELAPSE Electronics for pArticle PhysicS Experiments, Chile.
10. A Current Limiter Circuit with GaN Power Switch for Payloads Protection, Brazil.

A total of 24 PhD, 22 MsC, 8 undergraduate students, and 22 Professors participated in the 13 projects. In February 2021, US$25K were approved by EDS for the 2nd contest, which calls for projects to be launched in the second half of 2021.