

On December 13, 2014, the *VLSI Technology and Circuits Committee* met in San Francisco, California during the 2014 international ELECTRON DEVICES meeting on December 15-17. This committee has face to face meetings twice a year during the IEDM and VLSI Symposiums.

The objective of the *VLSI Technology and Circuits Committee* is to identify new and relevant areas of interest to the Electron Devices and Solid-State Circuits communities. Based on the nature of the areas, the committee recommends any or all of the following:

1. Initiate topical workshops of current interest (attached to existing conferences or incorporated in new ones)
2. Special issues for major publications (e.g., T-ED)
3. Panel session topics for major conferences
4. Special sessions for major conferences

The following topics were discussed during the December meeting:

1. A proposal to create a new conference in Asia (Asian Electron Devices Meeting) with focus on:
 - a. Device Technology
 - b. Industry (manufacturing) and academic related papers
 - c. Exhibition from equipment companies
 - d. Emerging devices, Bio/Medical and related topics
 - e. Tutorials
 - f. Location: Taiwan, China, Korea and Japan (First Round)

With valuable discussions with various EDS members including IEDM executives, EDS executives, AdHoc Committee members, the team decided to fine tune the proposal's mission and logistics in more detail and discuss further in the next committee meeting.

2. A special issue of IEEE Transaction on Electron Devices on "Variation Aware Technology and Circuit Co Design" was discussed. Dr. Seung-Chul Song from Qualcomm, Dr. Huiling Shang from IBM and Professor Kaustav Banerjee's are leading the efforts on this.
 - a. Final invited papers and topics were discussed
 - b. Deadline for reviewers and submitted papers were discussed and finalized
3. Membership policy change from three years to two years with potential of one renew was discussed.

Please contact Dr. Kaz Ishimaru (kazu.ishimaru@toshiba.co.jp), conferences/workshops subcommittee chair, or Dr. Shu Ikeda (shu.ikeda@tei-solutions.com), committee chair, for further information.

Reza Arghavani
EDS VLSI TC Committee Member
Lam Research Corporation
Fremont, CA, USA

Attendees (attached picture): Sitting from left to right - Ru Huang, Steve Chung, Jurriaan Schmitz, Shuji Ikeda, Hiroshi Iwai, John Suehle

Standing from left to right - Reza Arghavani, Hitoshi Wakabayashi, Seiichiro Kawamura, Kaustav Banerjee, Kazunari Ishimaru, Jacobs Swart and Mansun Chan (not in picture)

