

Introducing layered dielectrics in solid-state microelectronic devices

Biography

Mario Lanza is a Full Professor at Soochow University (world rank: 11th according to Shanghai Ranking, 19th according to US News, and 27th according to Nature Index). Dr. Lanza got his PhD in Electronic Engineering with honors in 2010 at Universitat Autònoma de Barcelona. During the PhD he was a visiting scholar at The University of Manchester (UK) and Infineon Technologies (Germany). In 2010-2011 he was NSFC postdoc at Peking University, and in 2012-2013 he was Marie Curie postdoc at Stanford University. Prof. Lanza has published over 100 research papers, including Science, Nature Electronics and IEDM, edited a book for Wiley-VCH, and registered four patents (one of them granted with 5.6 Million CNY). Prof. Lanza is member of the advisory board of several journals, including Advanced Electronic Materials, Scientific Reports, Nanotechnology, Nano Futures and Crystal Research and Technology, as well as guest editor of a special issue in Advanced Functional Materials. He is an active member of the technical committee of several world-class international conferences, including IEEE-IEDM, IEEE-IRPS, IEEE-IPFA and APS. Prof. Lanza has received the 2017 Young Investigator Award from Microelectronic Engineering (Elsevier), and the 2015 Young 1000 Talent award (among others), and in 2019 he was appointed as Distinguished Lecturer of the Electron Devices Society (IEEE-EDS). Since 2013, Prof. Lanza leads a research group formed by 15-20 PhD students and postdocs at Soochow University, and they investigate how to improve electronic devices using 2D materials, with special emphasis on two-dimensional (layered) dielectrics and memristors for non-volatile digital information storage and artificial intelligence computing systems.